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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

None

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

REPUBLIC OF KOREA 2002-72478 11/20/2002

verified

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	KOREA, REPUBLIC OF	5	25	5

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## TITLE

On-die termination circuit and method for reducing on-chip DC current, and memory system including memory device having the same

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